

♦ STRUCTURE Silicon Monolithic Integrated Circuit

♦ PRODUCT I<sup>2</sup>C BUS 64Kbit (8,192 × 8bit) EEPROM

♦ PART NUMBER BR24L64-W Series

PART NUMBER	PACKAGE
BR24L64-W	DIP8
BR24L64F-W	SOP8

♦ FEATURES Two wire serial interface

Wide operating voltage range (1.8V~5.5V) Endurance: 1,000,000 erase/write cycles

# ♦ ABSOLUTE MAXIMUM RATING (Ta=25°C)

Parameter	Symbol	Rating	Unit
Supply Voltage	Vcc	-0.3~6.5	V
Power Dissipation	Pd	800 (BR24L64-W) *1	11/
		450 (BR24L64F-W) *2	mW
Storage Temperature	Tstg	-65 <b>~</b> 125	°C
Operating Temperature	Topr	-40 <b>~</b> 85	°C
Terminal Voltage	_	-0.3∼Vcc+0.3	V

<sup>\*</sup> Degradation is done at 8.0mW/°C(\*1), 4.5mW/°C(\*2) for operation above 25°C

# ♦ RECOMMENDED OPERATING CONDITION

Parameter	Symbol	Rating	Unit
Supply Voltage	Vcc	1.8~5.5	٧
Input Voltage	VIN	0∼Vcc	٧

Status of this document

The Japanese version of this document is the formal specification.

A customer may use this translation version only for a reference to help reading the formal version.

If there are any differences in translation version of this document, formal version takes priority.



# ♦ MEMORY CELL CHARACTERISTICS (Ta=25°C, Vcc=1.8~5.5V)

Parameter			Specification					
Parameter		Min.	Тур.	Max.	Unit			
Write/Erase Cycle	*1	1.000,000	-	-	Cycles			
Data Retention	*1	40	-	-	Years			

Olnitial Data FFh in all address.

\*1 Not 100% TESTED

### ♦ DC OPERATING CHARACTERISTICS

(Unless otherwise specified Ta=-40~85°C, Vcc=1.8~5.5V)

Parameter		Specification			Unit	
Pittameter	Symbol	Min.	Тур.	Max.	Unit	Test Condition
"H" Input Voltage 1	VIHI	0.7Vcc	-	_	٧	2.5V≦Vcc≦5.5V
"L" Input Voltage1	VIL1	-	_	<b>0.3V</b> cc	٧	2.5V≦Vcc≦5.5V
"H" Input Voltage2	VIH2	0.8Vcc	_	-	٧	1.8V≦Vcc<2.5V
"L" Input Voltage2	VIL2	_	-	0.2Vcc	٧	1.8V≦Vcc<2.5V
"L" Output Voltage1	VOLI	-	_	0.4	٧	IOL≂3.0mA, 2.5V≦Vcc≦5.5V(SDA)
"L" Output Voltage2	VOL2	-	_	0.2	٧	IOL=0.7mA, 1.8V≦Vcc<2.5V(SDA)
Input Leakage Current	ILI	1	1	1	μА	VIN=0V~Vcc
Output Leakage Current	ILO	-1	1	1	μА	VOUT=0V~Vcc(SDA)
	ICC1	-	_	3.0	mA	Vcc=5.5V,fSCL=400kHz, tWR=5ms Byte Write,Page Write
Operating Current	ICC2	_	_	0.5	mA	Vcc=5.5V.fSCL=400kHz Random Read.Current Read.Sequential Read
Standby Current	ISB	-	_	2.0	μА	Vcc=5.5V,SDA,SCL=Vcc A0,A1,A2=GND,WP=GND

OThis product is not designed for protection against radioactive rays.

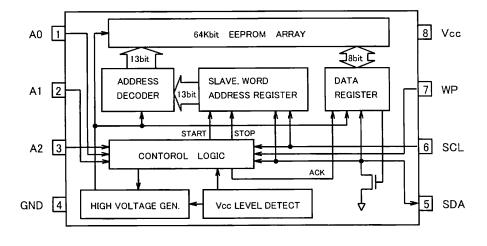
### ♦ AC OPERATING CHARACTERISTICS

(Unless otherwise specified Ta=-40~85°C, Vcc=1.8~5.5V)

(Unless otherwise specified			1a40~60 C, VCC=1.8~5.					
Symbol	FAST-MODE			STANDARD-MODE			Unit	
	2.5V≦Vcc≦5.5V			1.8V≦Vcc≦5.5V				
Junior	Min.	Тур.	Max.	Min.	Тур.	Max.	Onic	
fSCL	-	-	400	-	-	100	kHz	
tHIGH	0.6	-	-	4.0	-	-	μs	
tLOW	1.2	-	-	4.7	-	-	μs	
tR	-	-	0.3	-	-	1.0	µs	
tF	_	-	0.3	_	-	0.3	μs	
tHD:STA	0.6	-	-	4.0	-	-	μs	
tSU:STA	0.6	_	-	4.7	-	-	μs	
tHD:DAT	0	-	-	0	-		ns	
tSU:DAT	100	-	_	250	-		ns	
lPD	0.1	-	0.9	0.2	-	3.5	μs	
tDH	0.1	-	_	0.2	-	1	μs	
tSU:STO	0.6	-	-	4.7	-	-	μs	
tBUF	1.2	_	-	4.7	_	-	μs	
tWR	-	_	5	_	-	5	ms	
u	_	-	0.1	-	_	0.1	μs	
tHD:WP	0	_	-	0	-	1	ns	
tSU:WP	0.1	-	_	0.1	_	-	μs	
tHIGH:WP	1.0	_	_	1.0	<u> </u>	_	μs	
	Symbol  FSCL UHIGH  LLOW  LR  LF LHD:STA LSU:STA LHD:DAT LDH LSU:STO LBUF LWR LL LHD:WP LSU:WP	Symbol   FAX   2.5V   2.5V	FAST-M   2.5V ≤ Vcc   2.5V ≤	FAST-MODE   2.5V ≤ Vcc ≤ 5.5V   Min.   Typ.   Max.   fSCL	FAST-MODE         STANK           2.5V SVcc ≤5.5V         1.8V S           Min.         Typ.         Max.         Min.           fSCL         —         —         400         —           tHIGH         0.6         —         —         4.0           tLOW         1.2         —         —         4.7           tR         —         —         0.3         —           tHD:STA         0.6         —         —         4.0           tSU:STA         0.6         —         —         4.7           tHD.DAT         0         —         —         0           tSU:DAT         100         —         —         250           tPD         0.1         —         0.9         0.2           tDH         0.1         —         —         4.7           tBUF         1.2         —         —         4.7           tWR         —         —         5         —           tID:WP         0         —         —         0           tHID:WP         0.1         —         —         0.1           tHIGH:WP         1.0	FAST-MODE         STANDARD           SYMDOH         L8V ≤ Voc 2           L8V ≤ Voc 2         5.5V         Max.         Min.         Typ.           Min.         Typ.         Max.         Min.         Typ.           fSCL         —         —         400         —         —           tHIGH         0.6         —         —         4.7         —           tLOW         1.2         —         —         4.7         —           tR         —         —         0.3         —         —           tF         —         —         0.3         —         —           tHD:STA         0.6         —         —         4.7         —           tBUS:TA         0.6         —         —         4.7         —           tBUDAT         100         —         —         250         —           tBUF         0.1         —         0.9         0.2         —           tBUF         1.2         —         4.7         —           tWR         —         —         5         —         —           tWB         —         —	FAST-MODE         STANDARD-MODE           2.5V ≤ Vcc ≤ 5.5V           Min.         Typ.         Max.         Min.         Typ.         Max.           fSCL         —         —         —         Max.           fSCL         —         —         Max.           fSCL         — <t< td=""></t<>	

\*1 Not 100% TESTED

# ♦ BLOCK DIAGRAM



♦ PIN No., PIN NAME

PIN NAME
A0
A1
A2
GND
SDA
SCL
WP
Vcc

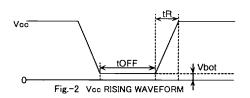
Fig.-1 BLOCK DIAGRAM



#### ♦ NOTES FOR POWER SUPPLY

Vcc rises through the low voltage region in which internal circuit of IC and the controller are unstable, so that device may not work properly due to an incomplete reset of internal circuit. To prevent this, the device has the feature of P.O.R. and LVCC. In the case of power up, keep the following conditions to ensure functions of P.O.R. and LVCC.

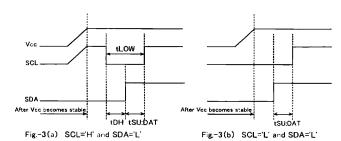
- 1. It is necessary to be "SDA='H'" and "SCL='L' or 'H'".
- 2. Follow the recommended conditions of tR, tOFF, Vbot for the function of P.O.R. during power up.



♦ RECOMMENDED CONDITIONS OF tR, tOFF, Vbot						
tR	tOFF	Vbot				
Below 10ms	Above 10ms	Below 0.3V				
Below 100ms	Above 10ms	Below 0.2V				

- Prevent SDA and SCL from being "High-Z".
   In case that condition 1. and/or 2. cannot be met, take following actions.
  - A) Unable to keep condition 1.

    ( SDA is "LOW" during power up.)
    - → Control SDA ,SCL to be "HIGH" as Fig.-3(a), 3(b).
  - B) Unable to keep condition 2.
    - → After power becomes stable, execute software reset.
  - C) Unable to keep both conditions 1 and 2.
    - → Follow the instruction A first, then the instruction B.



### **♦**CAUTIONS ON USE

(1) Absolute maximum ratings

If the absolute maximum ratings such as impressed voltage and action temperature range and so forth are exceeded, LSI may be destructed. Do not impress voltage and temperature exceeding the absolute maximum ratings. In the case of fear exceeding the absolute maximum ratings, take physical safety countermeasures such as fuses, and see to it that conditions exceeding the absolute maximum ratings should not be impressed to LSI.

- (2) GND electric potential
  - Set the voltage of GND terminal lowest at any action condition. Make sure that each terminal voltage is lower than that of GND terminal.
- (3) Thermal design
  - In consideration of permissible loss in actual use condition, carry out heat design with sufficient margin.
- (4) Terminal to terminal shortcircuit and wrong packaging
  - When to package LSI onto a board, pay sufficient attention to LSI direction and displacement. Wrong packaging may destruct LSI. And in the case of shortcircuit between LSI terminals and terminals and power source, terminal and GND owing to foreign matter, LSI may be destructed.
- (5) Use in a strong electromagnetic field may cause malfunction, therefore, evaluated design sufficiently.

# MHON

# ♦ PHYSICAL DIMENSION

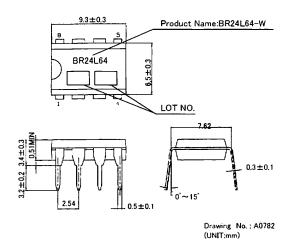


Fig.-4(a) PHYSICAL DIMENSION DIP8 (BR24L64-W)

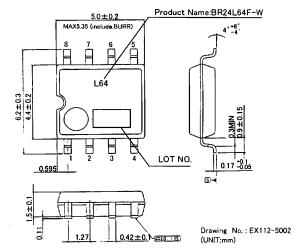


Fig.-4(b) PHYSICAL DIMENSION SOP8 (BR24L64F-W)

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